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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Alex S. Koh et al GROUP ART UNIT: 2123
APPLN. NO.: 09/847,487 EXAMINER: Kandasamy Thangavelu
FILED: May 2, 2001
TITLE: METHOD FOR PRODUCTIVE TEST PATTERNS FOR TESTING AN INTEGRATED
CIRCUIT

Certificate of Transmission under 37 CFR 1.8

I hereby certify that this correspondence is being facsimile
transmitted to the Patent and Trademark Office.

on October 24, 2005

Stacie Herrera
Signature

Stacie Herrera
Printed Name of Person Signing Certificate

**NOTICE OF APPEAL FROM THE EXAMINER TO THE
BOARD OF PATENT APPEALS AND INTERFERENCES**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SIR:

In accordance with 37 CFR 1.191, applicant hereby appeals to the Board of Patent Appeals and Interferences from the last decision of the examiner. The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. 503079, Freescale Semiconductor, Inc. This Notice is submitted in duplicate.

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Respectfully submitted,
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